TTC 2000 FIREBERD Data Error Analyzer

Features:

- Test Pattern: Pseudorandom sequences of 63, 511, 2047, 2^{15}-1, and 2^{20}-1; all marks, all spaces and 1:3; QRSS pattern provided by T1 interface
- Interface Emulation: DTE or DCE
- Bit Rate: 100 b/s to 14.0 Mb/s with appropriate interface
- Internal Standard Clock Frequencies: 1.2, 2.4, 4.8, 9.6, 19.2 56, 64 kHz, 1.544, 2.048, 3.152, 6.312 MHz
- External Clock: Accepts external clock through the data interface module or a rear panel BNC connector
- Frequency Counter: 200 Hz to 16 MHz
- Block Length: .4, 1, 4, 10, 40 and 100 k bits
- BER Test Length: 10^5, 10^6, 10^7, 10^8, 10^9 bits and Auto
- BER Range: 1.0 x 10^{-10} to 1.0 x 10^{-0}
- Error Insertion: Single logical errors or a constant 1.0 x 10^{-3} rate
- Real-Time Clock: Date and time clock, battery powered
- LED Indicators DTE Mode: Sync, Sync Lost, Gen Clk Actvy, DM (DSR), RR (RLSD), CS (CTS), TM, Rcv Data Inv, Rcv Data and Rcv Clock
- LED Indicators DCE Mode: Sync, Sync Lost, Gen Clk Actvy, TR (DTR), RS (RTS), LL, RL, Tx Data Inv, Tx Data and Tx Clock
- Printer Interface: RS-232 serial ASCII format
- Audio Indicator: Beeps on the occurrence of errors, sync losses, and BER measurement completion
- Interfaces (Optional): RS-232, V.35, MIL-188, RS-449 (442/423), TTL, DS0, T1, T1C, T2 AND G.703